

Fig.1a

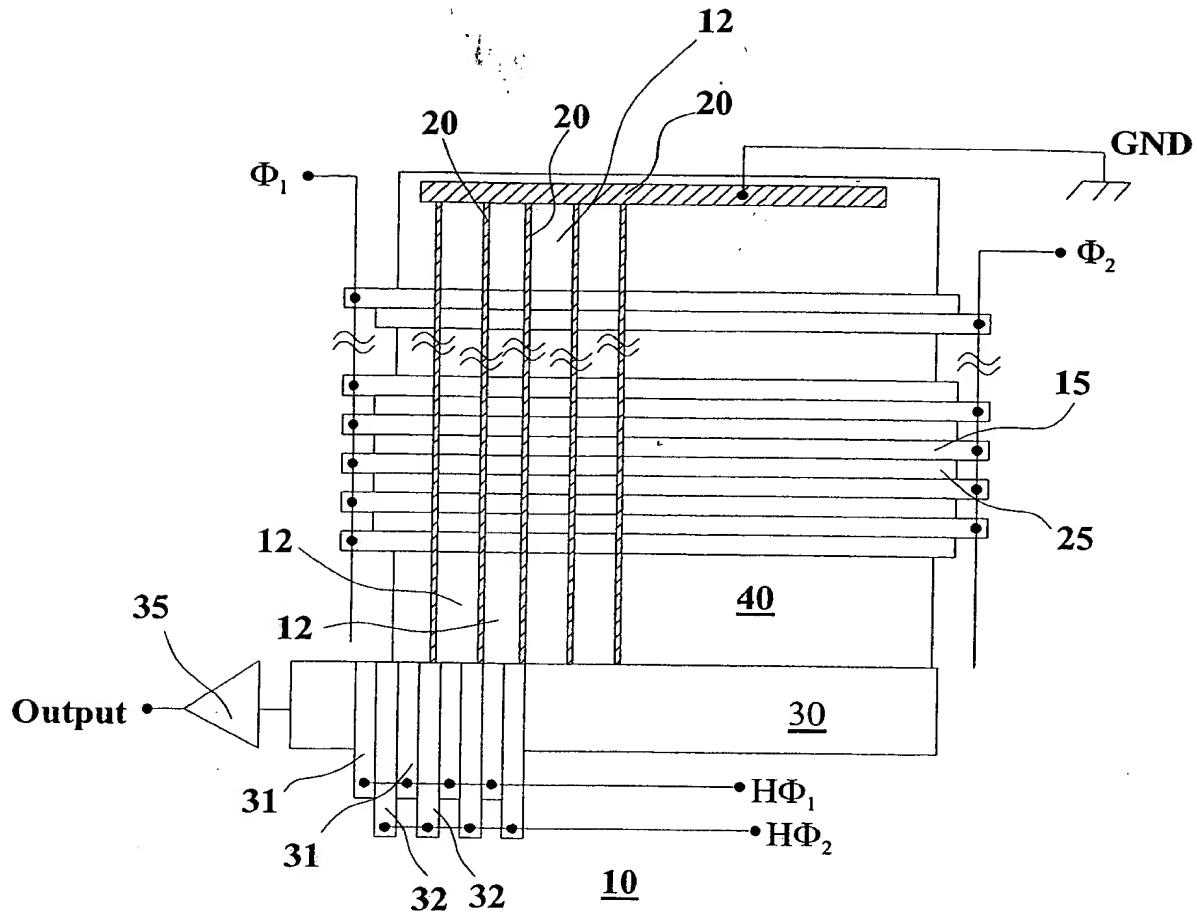


Fig.1b.

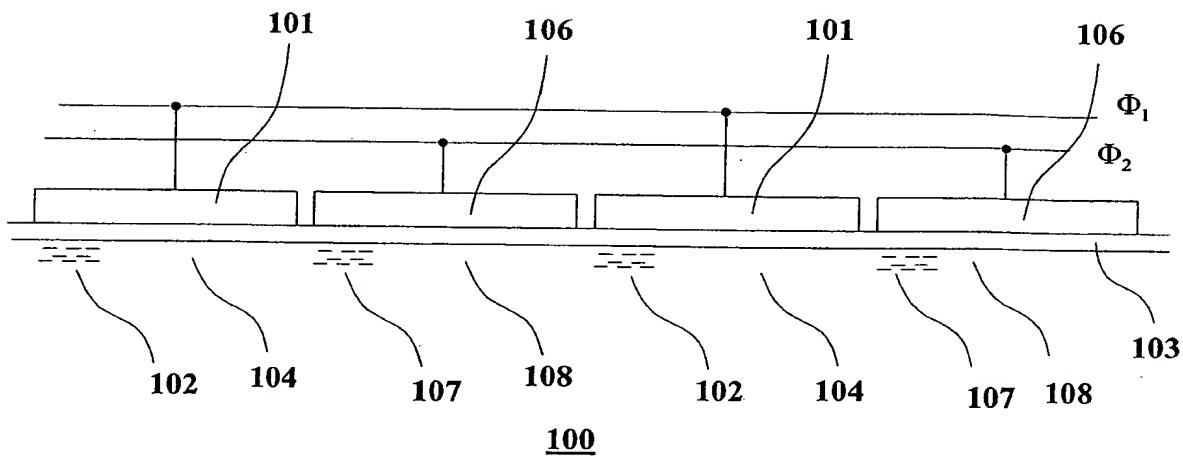


Fig.1c.

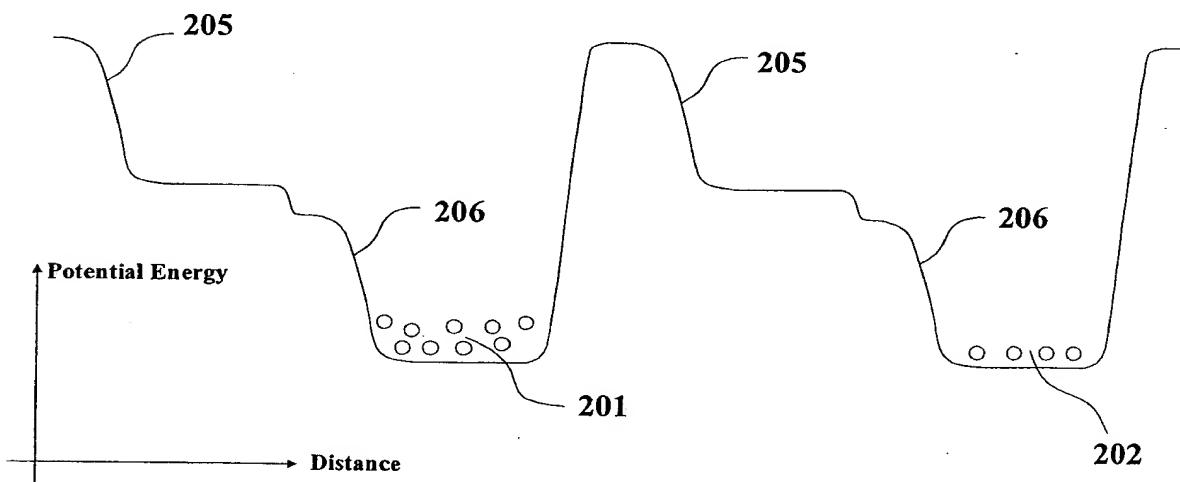


Fig.2a (Prior Art)

"Spill Backward" Accumulation Mode Clocking

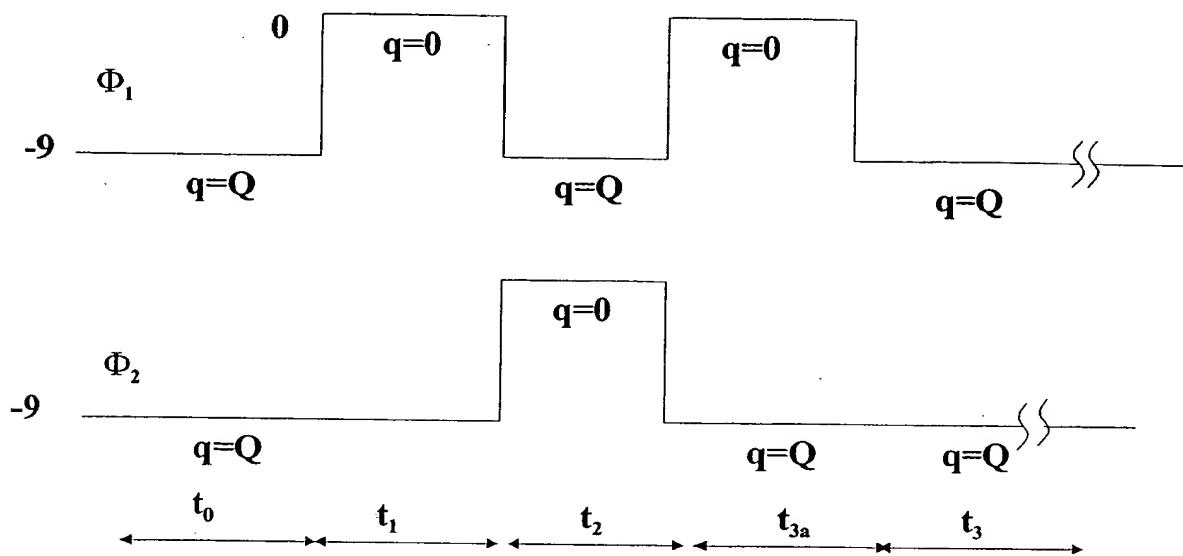


Fig.2b (Prior Art)

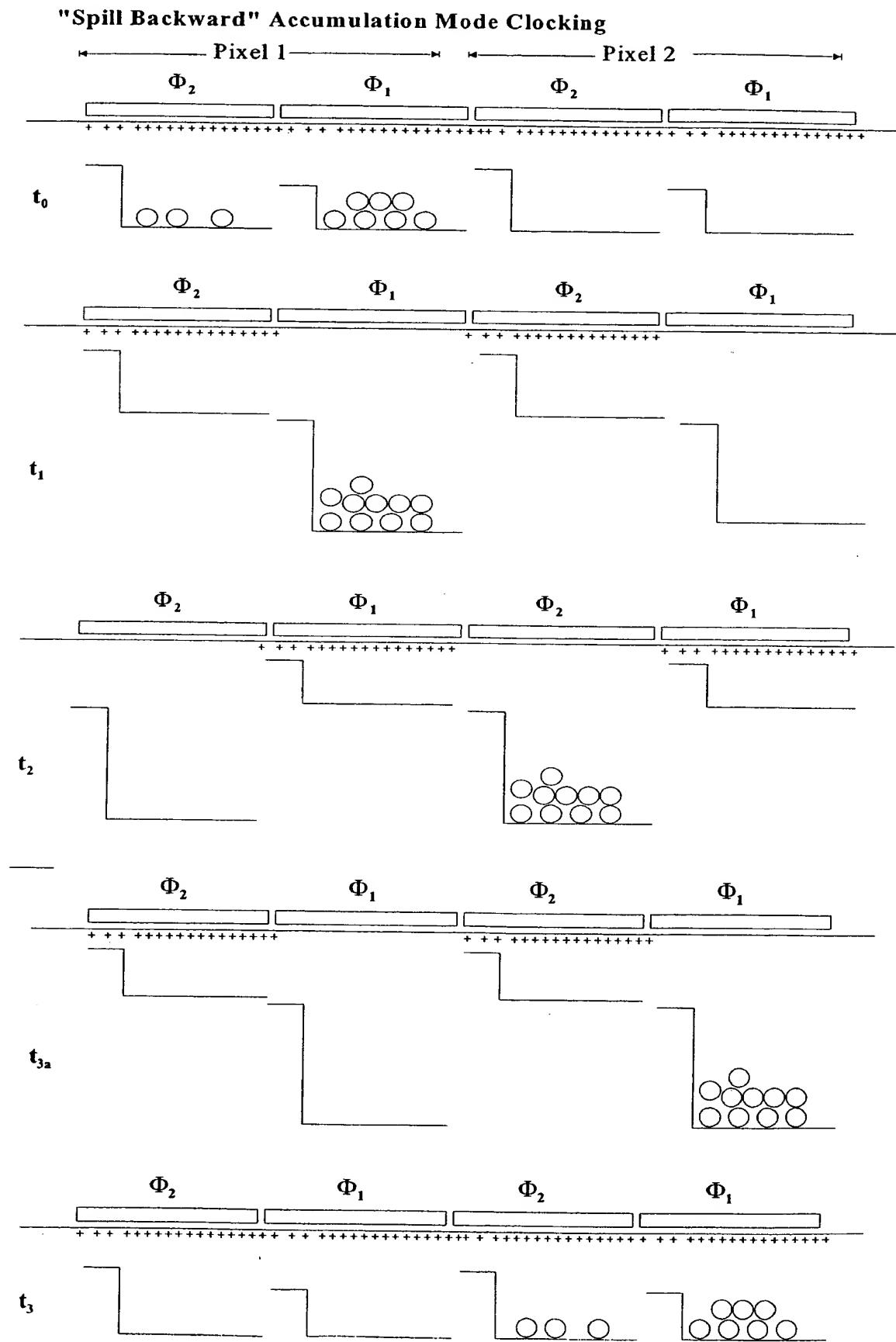


Fig.3a

Improved "Spill Backward" Accumulation Mode Clocking

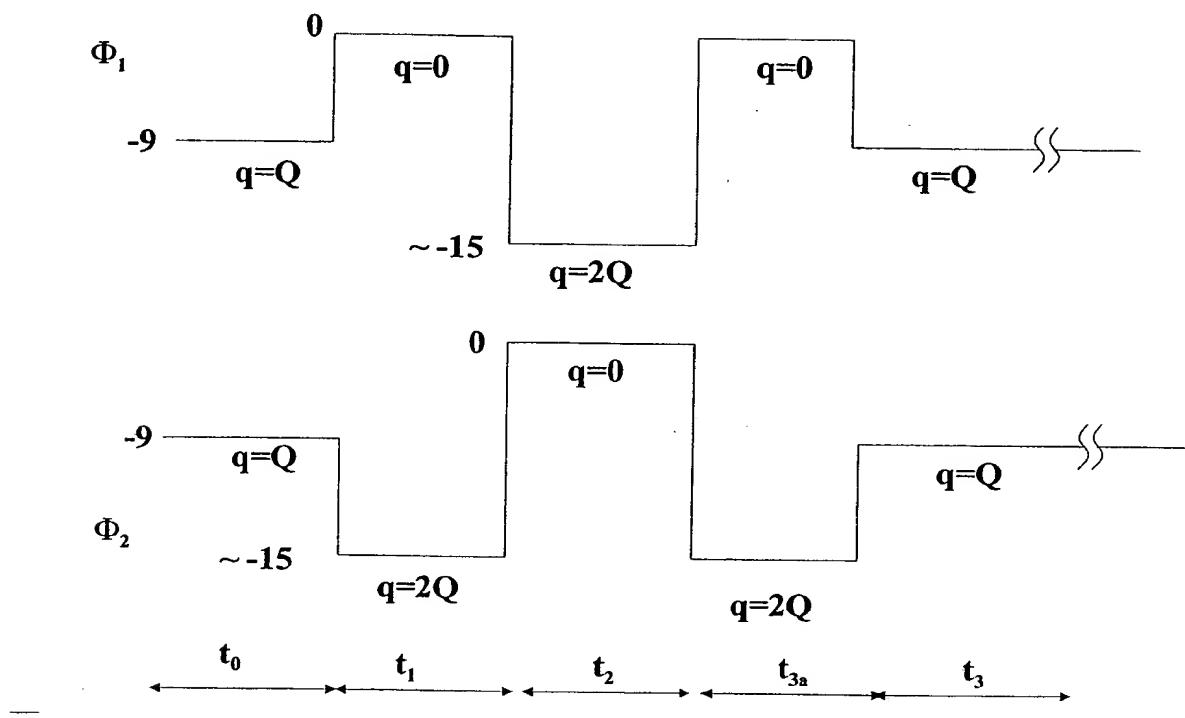


Fig.3b.

Improved "Spill Backward" Accumulation Mode Clocking

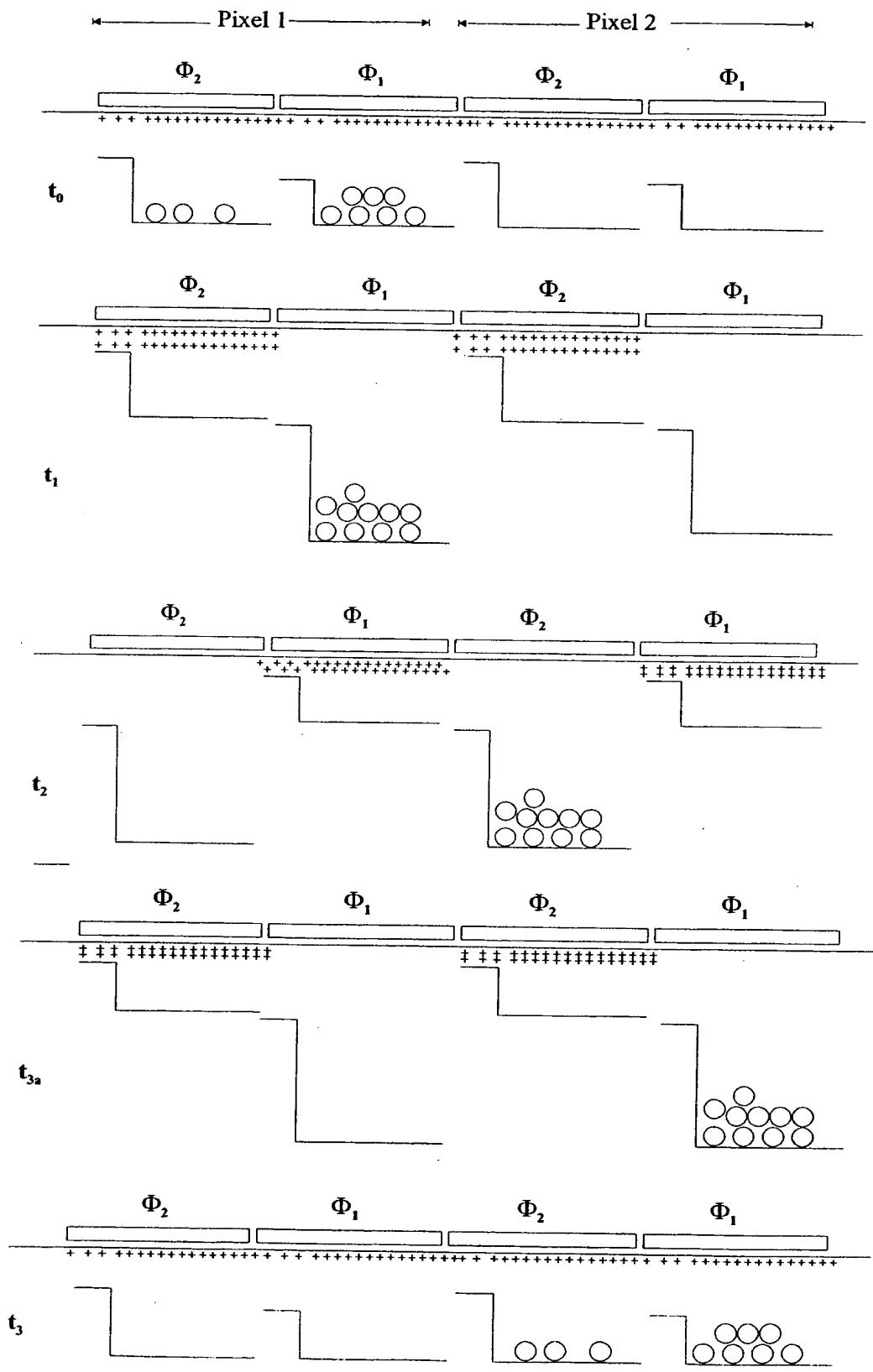


Fig.4a (Prior Art)

"Spill Forward" Accumulation Mode Clocking

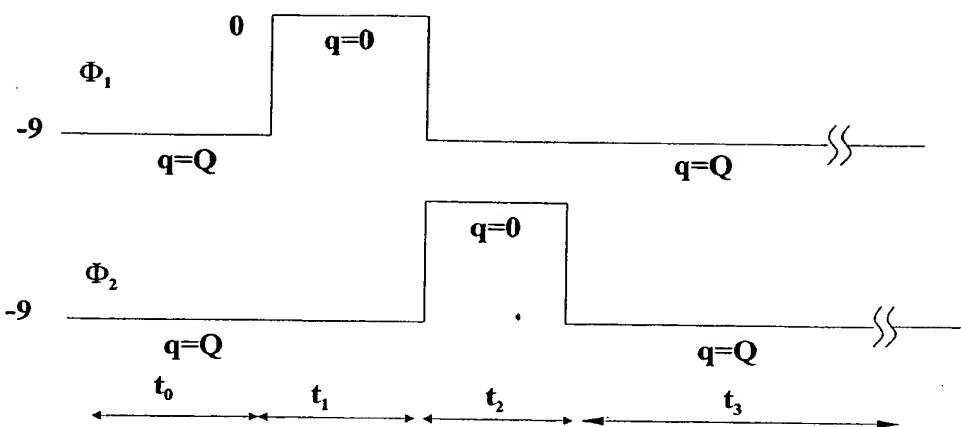


Fig.4b (Prior Art)

"Spill Forward" Accumulation Mode Clocking

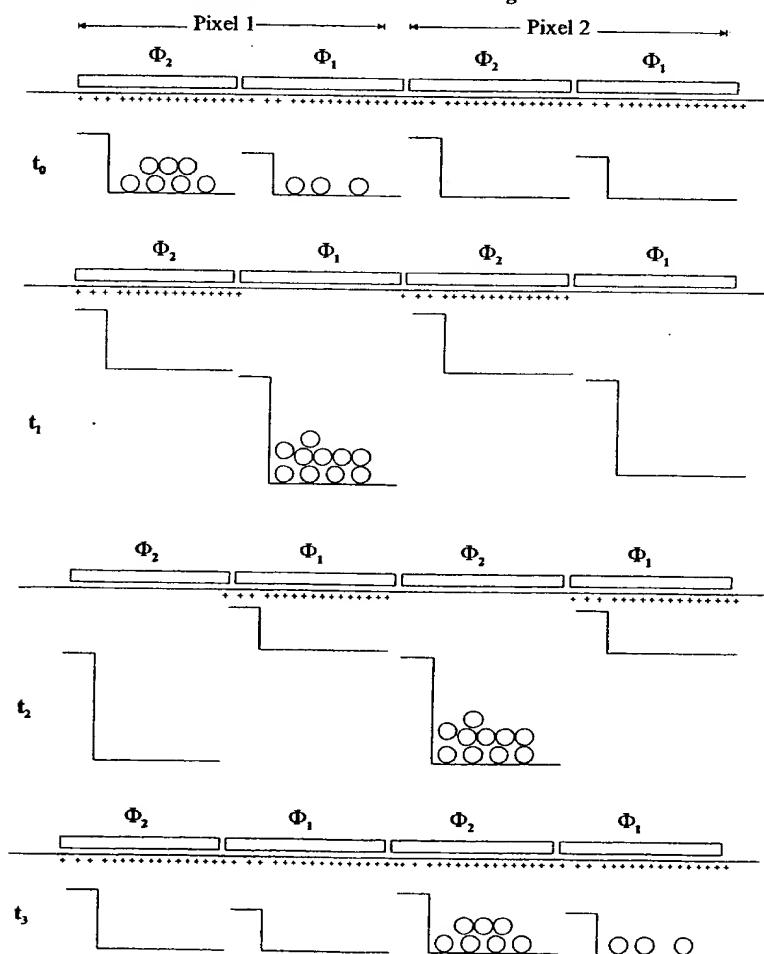


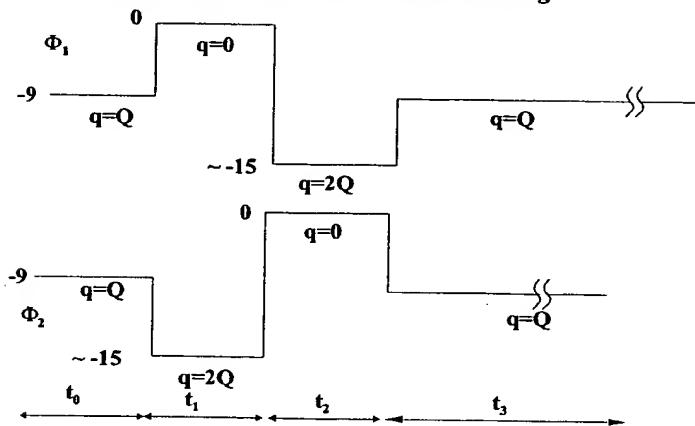
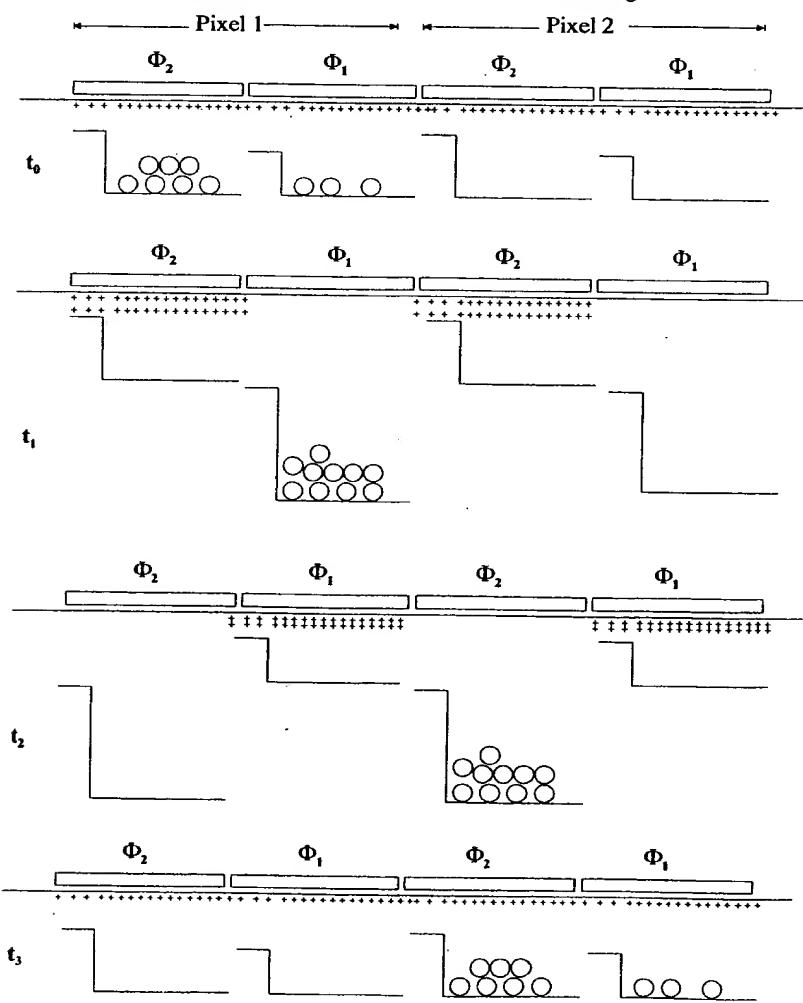
Fig.5a**Improved "Spill Forward" Accumulation Mode Clocking****Fig.5b****Improved "Spill Forward" Accumulation Mode Clocking**

Fig.6

Accumulation Mode Clocking with Photodiode Read-out

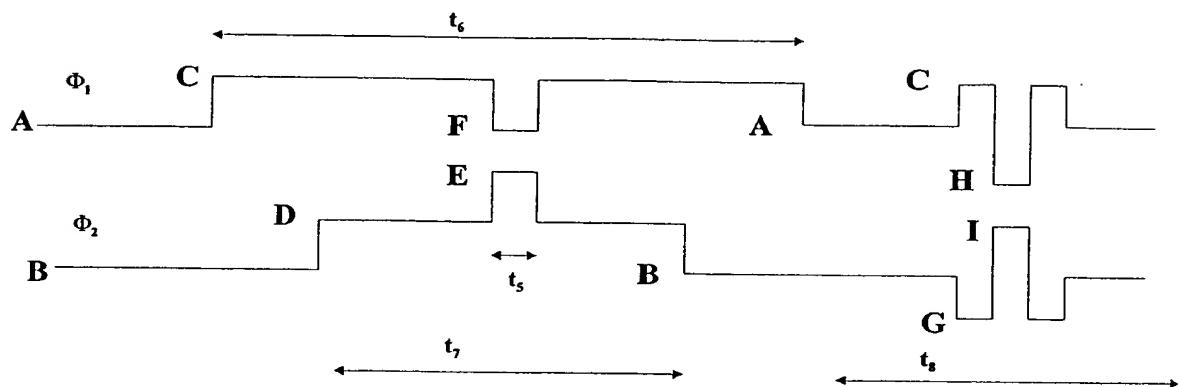


Fig.7.

